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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Raymond J. Beffa

Serial No.: 10/092,185

Filed: March 6, 2002

For: METHOD FOR SORTING
INTEGRATED CIRCUIT DEVICES

Confirmation No.: 1655

Examiner: J. Rodriguez

Group Art Unit: 3653

Attorney Docket No.: 2269-3037.10US
(95-1074.10/US)

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In compliance with the duty to disclose information material to patentability pursuant to 37 C.F.R. § 1.56, it is respectfully requested that this Supplemental Information Disclosure Statement be entered and the documents listed on attached Form PTO-1449 or PTO/SB/08 be considered by the Examiner and made of record. Copies of the listed documents are enclosed pursuant to 37 C.F.R. § 1.98(a).

In accordance with 37 C.F.R. § 1.97(g) and (h), filing of this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made or an admission that the information cited herein is, or is considered to be, material to patentability as

defined in 37 C.F.R. § 1.56(b). Further, no representation is made by Applicant herein that no other possible material information as defined in 37 C.F.R. § 1.56 (b) exists.

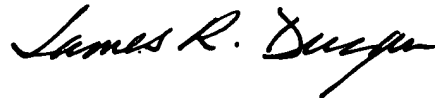
U.S. Patent Documents

<u>U.S. Patent No.</u>	<u>Publication Date</u>	<u>Patentee</u>
US - 5,326,709	07/05/1994	Moon et al.
US - 6,788,993 B2	09/07/2004	Beffa

Applicant offers to supply any explanation or discussion of the documents which the Examiner feels is necessary or desirable and which is requested.

This Supplemental Information Disclosure Statement is filed concurrently with an RCE in the above-identified application, and therefore no additional fee is due.

Respectfully submitted,



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Date: February 4, 2005

JRD/dlm:lmh

Enclosures: Form PTO-1449 or PTO/SB/08
Copy of documents cited

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